



"ADL generator"

Web - Images - Groups - Directory - News - Searched the web for "ADL generator".

Results 1 - 7 of about 9. Search took 0.13 seconds.

### Using ADL/Together to define XXXG4SimEvent

... Now the ADL files should have been generated: XXXHt.adl #ifndef XXXHt\_\_ADL#define XXXHt\_\_ADL/\* @Title: Module ADL generator for Together \* @Author ... www.physik.uni-muenchen.de/~schieter/adltogether.html - 25k - Cached - Similar pages

### Technical Annex 2 to SWA 2001/01: Data Dictionary

... Data Object Adapter Generator Deployed: Prototype Reverse Engineering (C++) ADL Generator April 2002: ADL Reflection API Deployed ... www.usalas.bnl.gov/computing/mgmt/datadic annex-07Jul01.html - 15k - Cached - Similar pages

### [PDF] Minutes of the DD meeting: LAPP – 09/02/2001-02-12

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... ContainedObject or ObjectVector<> do we define the type of the transient object inheritance in the description or - as an "ADL generator" option Not sure ...

atlas.web.cern.ch/.../SOFTWARE/Oararchitecture/DataDictionary/Meetings/DD\_Group/2001/Minutes\_090201.pdf - Supplemental Result - Similar pages

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### [PDF] Athena Data Dictionary and Description Language

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... Reverse Engineering (C++) ADL Generator — A code generator which parses declarative C++ (ie header-like C++) and produces compatible ADL... atlas.web.cern.ch/Atlas/GROUPS/SOFTWARE/Oararchitecture/DataDictionary/Meetings/Others/SW\_15may01.pdf

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### [PDF] Component Industry Promotion Component Industry Promotion in Korea ...

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Page 1. 1 KAIST EEC/SSE Component Industry Promotion Component Industry

Promotion in Korea: Past, Present, and Future in Korea: Past ...

www.cbop.gr/jpd/semin/pose/2002/bosz2002\_bae.pdf - Similar pages

### [PDF] THE FLORIDA STATE UNIVERSITY COLLEGE OF ARTS AND SCIENCES ...

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Page 1. THE FLORIDA STATE UNIVERSITY COLLEGE OF ARTS AND SCIENCES CUSTOMIZABLE

LEARNING OBJECTS METADATA AUTHORIZING BY ELENA VALENTINA ...

ed.lib.fsu.edu/theses/available/edlib-09282003-172442/unrestricted/01\_evn\_thesis.pdf - Similar pages

### [PDF] 소 프 티 웨 어 공 학 국 채 과 쟤 개 발 협 회 및 방 향

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... (PSM Refiner), 코드 생성기, (Code Generator), 이커 텍 씨 구축기, ADL 씨 풍기, (ADL Generator), 이커 텍 씨 모 델 편집기, (Architecture Model Editor), ...

selab.catetgu.ac.kr/conference/2002\_KSEJW/paper/TTechnology.ppt - Supplemental Result - Similar pages

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If you like, you can repeat the search with the omitted results included.

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Searched the web for "HDL generator" opcode.

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**[HDL generator] opcode** **Google Search**

**[PORT]** [HDLGen: Architecture Description Language driven HDL Generation...](#)

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... (OPCODE) add (OP\_TYPE DATA\_OP) (OPERANDS (SRC1 int\_rf) (SRC2 int\_32)) (DST int\_rf ... result of an analysis and optimization process of the HDL generator HGGEN, the ...

[www.cse.uci.edu/technical\\_report/TR03-04.pdf](http://www.cse.uci.edu/technical_report/TR03-04.pdf) - Similar pages

**[PORT]** [Outline Motivation Architecture Description Language \(ADL\) The...](#)

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... Compiler Synthesis Feedback ( Area Power, Clock Frequency ) HDL Generator Hardware Model ... 21 ADL Specification of the DLX Processor ((OPCODE OPCODE ADD ADD ...

[www.rsp-workshop.org/History/slide03/session-10-p3.pdf](http://www.rsp-workshop.org/History/slide03/session-10-p3.pdf) - Similar pages

**[PORT]** [MetaCore An Application Specific DSP Development System](#)

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... neccesity synthesis in the macro block generation step the HDL generator generates each ... is used for the pipeline design in this model the opcode is transmitted ...

[www.sigae.org/Archives/Proceedings/Dau/Daeg/papers/1998/dacg/pdfs/48\\_2.pdf](http://www.sigae.org/Archives/Proceedings/Dau/Daeg/papers/1998/dacg/pdfs/48_2.pdf) - Similar pages

**[PORT]** [IISA](#)

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... (bif) Opcode,.. short operandA,.. short operandB,.. };... This enables the HDL generator to generate the appropriate HDL code from a more abstract specification,...

[www.cse.iitd.ernet.in/esproject/homepage/docs/seminars/makulasset\\_group.ppt](http://www.cse.iitd.ernet.in/esproject/homepage/docs/seminars/makulasset_group.ppt) - Similar pages

**[PPPT]** [Retargetable Compilers](#)

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Embedded Systems Design Workshop IIT Delhi, Jan 2002 © R. Leupers, Retargetable Compiler Technology, Rainier Leupers, Aachen University of Technology, ...

[www.cse.iitd.ernet.in/esproject/workshop-2002/speakers/rainier/slides/iit\\_tutorial.ppt](http://www.cse.iitd.ernet.in/esproject/workshop-2002/speakers/rainier/slides/iit_tutorial.ppt) - Similar pages

**[PORT]** [A RETARGETABLE TOOL-SUITE FOR THE DESIGN OF APPLICATION SPECIFICIC...](#)

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... assembler and linker, but significant work was not reported on the HDL generator. .... 3. Instruction mode operands (OPERAND) 4. Instruction mode opcode (CODING) 5 ...

[www.sdf.usc.edu/~asyed/1622-ISCAS.pdf](http://www.sdf.usc.edu/~asyed/1622-ISCAS.pdf) - Similar pages

**[PORT]** [A Methodology for the Design of Application Specific Instruction...](#)

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... which forward information about the instruction such as instruction opcode, operand registers ... which are in a subsequent phase of the HDL generator run filled ...

[https://www.coware.com/pls/cust\\_portals/docs/PAGE1\\_COWAREDOCUMENT/PDFtechpaper\\_aachen3.PDF](http://www.coware.com/pls/cust_portals/docs/PAGE1_COWAREDOCUMENT/PDFtechpaper_aachen3.PDF) - Similar pages

**[PORT]** [C / C : A Retargetable Tool-Suite for Embedded Processors](#)

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... June 2003 • G O : an HDL generator that generates a synthesisable VHDL model ... constraints often occur in application-specific processors, to save opcode space ...

[www.rettarget.com/doc/target-whitepaper.pdf](http://www.rettarget.com/doc/target-whitepaper.pdf) - Similar pages

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